

**PCT**WORLD INTELLECTUAL PROPERTY ORGANIZATION  
International Bureau

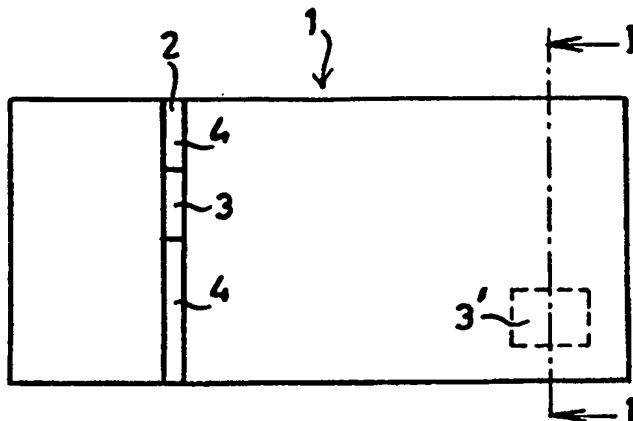
## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification <sup>6</sup> : <b>G06K 19/02, 19/077</b>		A1	(11) International Publication Number: <b>WO 99/54842</b> (43) International Publication Date: 28 October 1999 (28.10.99)
(21) International Application Number: <b>PCT/NL99/00220</b>		(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).	
(22) International Filing Date: 15 April 1999 (15.04.99)			
(30) Priority Data: 1008929 20 April 1998 (20.04.98) NL			
(71) Applicant (for all designated States except US): VHP VEILIGHEIDSPAPIERFABRIEK UGCHELEN B.V. [NL/NL]; Hoenderloseweg 84, NL-7339 GJ Apeldoorn (NL).			
(72) Inventors; and			
(75) Inventors/Applicants (for US only): KRUL, Johannes [NL/NL]; Meerkotlaan 117, NL-6641 ZB Beuningen (NL); de HESSE, Wilhelm, Bernardus [NL/NL]; Parkelerweg 20, NL-7391 ET Twello (NL).			
(74) Agent: VOLMER, J., C.; Exter Polak & Charlouis B.V., P.O. Box 3241, NL-2280 GE Rijswijk (NL).			

(54) Title: SUBSTRATE WHICH IS MADE FROM PAPER AND IS PROVIDED WITH AN INTEGRATED CIRCUIT

## (57) Abstract

The invention relates to a substrate (1) which is made from paper and is provided with at least one integrated circuit (3) which is produced from a semiconductive organic polymer. A semiconductive organic polymer of this nature, when used as the base material for the integrated circuit (3), leads to the possibility of directly producing the substrate (1) in the required thickness, to the need for support layers and/or protective layers being eliminated, and to the possibility of reducing the cost price of the substrate (1) compared to substrates which comprise an integrated circuit (3) of the silicon type.



BEST AVAILABLE COPY

**FOR THE PURPOSES OF INFORMATION ONLY**

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakhstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		

Substrate which is made from paper and is provided with an integrated circuit

The invention relates to a substrate which is made from paper and is provided with at least one integrated circuit.

A substrate of this nature is known from German Patent Application DE-196 01 358, and is used in security documents and banknotes to protect against forgery and fraud. This known substrate comprises an integrated circuit which is incorporated in the substrate and contains predetermined data. The IC is contactlessly readable and is joined to the substrate in a non-releasable manner. The IC which is used in this substrate is a conventional IC, i.e. of the well-known silicon type. The dimensions of the chip originally produced are reduced by means of etching or polishing, so that the chip acquires the thickness which is desired for it to be incorporated in the paper mass. In order to prevent damage to the crystalline circuits, the IC is reinforced with the aid of a support layer, which also serves to position the IC. Furthermore, the IC is covered with a protective, chemically resistant layer. The lack of flexibility of this known silicon chip is a drawback when a substrate of this nature is used as a security paper, for example in banknotes and identity documents. In addition, the extra layers which are to be included, as well as the additional processing step for producing the appropriate dimensions, lead to an additional increase in the cost price of a substrate of this nature.

The object of the present invention is to provide a paper-based substrate for use in security documents, banknotes and the like, in which an integrated circuit is incorporated, which substrate does not have the drawbacks mentioned above.

According to the present invention, this object is achieved with a substrate of the type mentioned above in which the integrated circuit comprises a semiconductive organic polymer. This means an electronic circuit which is arranged in the polymer material and the contents of which are programmed in order to assign it a specific function. Polymer chips of this nature are highly flexible and are therefore eminently suitable for use in security documents such as banknotes. Even sharp creases in the chip made from a semiconductive organic polymer

do not impede the functioning of the chip. In addition, polymeric ICs can be produced directly in the desired dimensions, in particular with regard to thickness, and the costs of a chip of this nature are lower by approximately a factor of 10 than the 5 current lowest price for a chip of the silicon type.

In the polymeric chip, the nonconductive support on which the semiconductive polymer material is deposited substantially determines the thickness of the overall integrated circuit. It is preferable to use a mechanically strong insulator: plastics with strong intramolecular and intermolecular interactions are particularly suitable for this purpose. 10

Using an IC of this nature as a security mark in security paper and similar items provides novel and powerful means of protection, since producing these ICs is much too 15 complicated for forgers and generally far exceeds their knowledge and capabilities.

In the context of this application, paper is understood to mean paper which is made from natural or synthetic fibres, as well as "paper" which can nowadays be produced from 20 plastic films, which paper is used for the production of security paper, banknotes and the like.

The integrated circuits may number one or more and may be adjusted as a function of the requirements. For example, with a view to certainty of operation, it is possible to incorporate 25 two or more identical polymer chips, so that in the event of one of these chips failing the substrate and/or the end product which has been produced therefrom can still be used.

Preferably, the organic polymer is selected from conjugated polymers, in particular from oligomeric pentacene, 30 poly(thienylene vinylene) or poly-3 alkylthiophene. An IC which is produced from one of these materials is described by Brown et al. in *Science*, 270, pp. 972-974, 1995.

As will be understood by the skilled person, the plastic IC, used in the invention, comprises additional layers 35 of different polymers besides the semiconductive polymer layer. For example, the substrate may be produced from polyimide, on which polyaniline blocks are formed, which function as source and drain. On top thereof, the semiconductive polymer layer is present, e.g. comprising poly(thienylene vinylene). This layer 40 is covered by an insulating layer, e.g. from polyvinylphenol,

while a top layer of polyaniline is the uppermost layer, which is the gate.

5 In an embodiment of the substrate according to the invention, the integrated circuit is contactlessly readable, the data transmission being brought about by an inductive or capacitive route, as is known in the prior art.

10 In case of inductive readout a coil is needed for the current supply, which has to be connected conductively to the IC; thereby making possible readout from a distance. In order to be able to readout at a small distance it is necessary that the 15 IC contacts a conductor, wherein this conductor together with the measuring apparatus creates a capacity, whereby the current supply and readout become possible.

According to another preferred embodiment of the 20 substrate according to the invention, the substrate comprises a conductive security thread which is connected to the integrated circuit, which security thread serves as a direct contact or indirect contact for the readout and for supplying the current. In a preferred embodiment thereof, the security thread is 25 metallized in order to provide the required electrical conductivity, except at the position of the polymeric IC, where the deposition of metal is interrupted. In the case of direct current supply the metal has to be accessible. Possible ways of providing this accessibility comprise a security thread which is incorporated in the substrate, as well as a security thread which is incorporated in the substrate and the metal parts of 30 which are accessible via so-called windows. Advantageously, one or more integrated circuits are part of the security thread itself. The thickness of this security thread may be adapted to the intended use of the substrate, for example in banknotes. For banknote paper, the thickness of the paper substrate usually lies in the range up to 100 µm. In this case, the thickness of the security thread preferably lies in the range of 15-60% of the thickness of this substrate. If the paper substrate has a 35 different thickness, as for example for a cover of an identity document such as a passport, a minimum thickness of the security thread of approximately 10 µm applies. A thickness greater than 100 µm is relatively meaningless for use in security paper. The preferred embodiment of a polymeric IC in the form of a security 40 thread provides an additional security feature which can easily

be recognized by the public. The thread comprising an integrated circuit may additionally comprise a number of other features, such as a dye, fluorescent or phosphorescent material, luminescent material and printed indices.

5       Organic, conductive polymers can also be used for supplying the current to the chip, although in case of direct contact the mechanical contact properties of these polymers currently still leave something to be desired.

10      A simple security thread which consists of conductive polymers is proposed in European Patent Application EP-A-0,753,623. However, a thread of this nature has only conductive properties. There are no semiconductor properties, and therefore it is not possible to apply and store a code in a manner which is comparable to a conductive polymer thread in  
15     which an integrated circuit is incorporated. The security thread comprising an integrated circuit according to the invention can be arranged in the customary way, for example by being incorporated completely or integrated in the paper mass, in a window or by being attached to the surface of a document. If protection  
20     against attack from chemicals is required, a chemically resistant, electrically nonconductive protective layer may be applied to the conductive organic polymers of the chip.

25      The polymeric chip does not per se have to be completely incorporated in the paper, as is the case for the silicon chip in the German patent application mentioned above. As an alternative, the polymeric IC may also be arranged on the surface of the substrate, using customary techniques for attaching foils, holograms, other optically active elements and the like.

30      The integrated circuit may also advantageously itself form part of all sorts of optically active elements, such as foils, patches, holograms or kinograms, which are arranged on or in the substrate as additional security features. As has already been described above with regard to a security thread, according  
35     to a further preferred embodiment it is also possible to shape optically active elements of this nature in such a manner that two electrically separate conductive parts of such elements function for readout and current supply, both directly and capacitively. The conductive parts may consist of metal, conductive polymer or a combination thereof.

For protection purposes, the integrated circuit may comprise a preprogrammed code, which code is applied before the chip is incorporated in the substrate.

Advantageously, the integrated circuit comprises a code of an intrinsic property of the substrate in which the circuit is incorporated.

In the current state of the art, the polymer IC may be used only in one direction; i.e. it can be written or programmed once. A preferred way of storing a code in the IC is to use techniques which are derived from cryptology. The authentic code is then stored in the IC in an encrypted manner, and deciphering is impossible without knowing the secret key. Thus, even if unwritten chips could be obtained illegally, the secret key forms a powerful and virtually impregnable barrier preventing the forger from applying a message to security documents and reading this message out. The protection can be improved still further if the optionally partial programming of the chip is carried out after the IC has become part of the security document, as will be explained in more detail below.

The shape of the polymer chip is not critical. Currently, a dimension of approximately 1 mm for a rectangular shape represents the lower limit of a surface dimension if a reasonable number of bits are to be stored in the IC. A rectangular IC of 4 mm by 6 mm currently holds approximately 48 bits, i.e. two bits/mm<sup>2</sup>. The ratio of the surface dimensions for a polymer chip (i.e. length to width) should preferably not exceed 10:1, owing to the resultant undesirable build-up of the chip given larger ratios. The small dimensions of an IC offer the possibility of covering the chip with additional features which are customarily used in the prior art. The dimensions of such additional features are generally large compared to the dimensions of the polymeric IC. Thus even large ICs, with sufficient memory capacity for storing large quantities of data, can be used without impairing the appearance of the security paper. If a combination of a chip with another security mark is arranged on the security paper, it is necessary to ensure that reading and supplying current to the chip are not adversely affected by an additional security feature of this nature.

The substrate comprising a polymer IC according to the invention is used as a security paper in, for example, bank-

notes, passports, identity cards and other security documents, such as securities.

5 The development of an inexpensive integrated circuit of this nature offers a number of novel possibilities for preventing forgery of security documents, beginning with a completely new type of electronics (electronic bar codes) in security paper.

10 Examples of the use of an IC as a security feature in a document which will be described are the various possibilities for a banknote, but comparable possibilities likewise exist for other types of security documents, such as passports, identity cards and the like.

15 A first possibility relates to the use of a completely preprogrammed IC in the substrate made from paper. The IC contains one or more codes, if desirable in encrypted form, relating to the banknote. This information may comprise the value, the country, the place and/or time of production, number and the like. For a specific value of the banknote, the information on each chip is substantially identical, i.e. value, 20 country and usually the paper manufacturer and/or printer, and partially different, i.e. production time, production numbers and sometimes paper manufacturer and/or printer.

25 A more specific protection is obtained with a chip which is partially preprogrammed with a unique code (first code) and an additional second code. This second code is an encrypted translation of the first code. Encryption is carried out using a first key. In the event of verification, the second code is read and the encrypted relationship to the first code is verified using a second key. The second code can be applied to the chip 30 before or after the chip has been arranged in the substrate. An encryption system of this nature is described, by way of example, in WO-A-97/24699.

35 In this known system, an intrinsic property of the object is coded, encrypted and enciphered. For banknotes, surface properties are taken at a special location, coded, encrypted and stored as a printed pattern on the banknote. In the event of verification, the printed pattern and the surface property are compared with one another using a second key.

40 Many other properties, as well as properties distributed arbitrarily in the substrate, are used in the prior art for

the protection of security documents, cf., *inter alia*, WO-A-91/19614 (fibre direction), GB-A-230,407 (reflective flakes), US-A-4,218,764 (magnetic particles or fibres) and WO-A-87/01845 (conductive fibres). In all these cases, arbitrary and therefore unique properties of a document are used for the verification. Hitherto, there has been no suitable chip available for use in paper substrates in order to store the (encrypted) code, and consequently a coded property was always stored in another way, for example outside the document itself, or was printed in or on the document or recorded magnetically therein. The polymeric chip which is used in the substrate according to the invention makes it technically possible to use and store these protection features inside the document.

The fluorescence properties of arbitrarily distributed fluorescent fibres in a predetermined area of the banknote may be a suitable property. However, any other property which can be measured and is arbitrarily distributed in or on the paper may also be used. A condition is that the property used must be stable throughout the entire lifespan of the document, which means that any property which is highly dependent on the consequences of use, such as soiling, contamination, creasing and the like, is in principle unsuitable.

The coordinates of the relevant part of the banknote where the arbitrary property is determined and, if necessary, the orientation in which the surface must be examined may also be stored in the chip. Thus, when verifying the banknote, a specific parameter is measured along a predetermined path, or an image of the entire banknote is taken, but the assessment is carried out using only the data found at the precoded coordinates. The result of this measurement is compared with the stored code, which likewise refers to the same property at the same location. On the basis of this comparison, which may optionally be encrypted, a rejection or acceptance signal is generated.

The substrate with a polymeric IC according to the invention may furthermore comprise customary security features, such as watermarks, security threads, optically active elements and special chemicals, microprints, etc., standard techniques being used to determine these features.

The invention also relates to a security thread or

optically active element comprising an integrated circuit made from a semiconductive organic polymer.

The following example illustrates the invention. In this case, specific fluorescent properties in a specific part of the document are used as an example. Many banknotes are supplied with a number of highly fluorescent fibres which emit different colours of light. These fibres are distributed arbitrarily through the document. The local fluorescence of various types of fibres at a predetermined location can be coded and digitally stored in the chip, optionally in encrypted form, at the time the document is produced, i.e. during the paper manufacturing phase or during the document printing phase. In the event of verification, the area in question is read again using the coordinates and orientation which are stored in the chip, and the results are compared with one another, followed by rejection or acceptance. The said coordinates and orientation will generally differ for each separate banknote, with the result that verification is completely unique for the said document, since the arbitrary property and coordinates are unique for that document. In this way, the chip of each separate banknote contains a unique code which, as it were, represents a fingerprint of a specific part of the banknote in question. The code of the intrinsic property may be stored either in encrypted or nonencrypted form.

As has already been mentioned above, the use of the substrate according to the invention is not limited to banknotes. For other uses, such as passports and identity documents, part of a biometric property of the legal owner can be used to generate a digital code which is then stored in the IC of the document. One such example could be a coded part of a digitized photograph of the legal owner, the part to be digitized being determined by coded parameters which are unique for each document. Entirely as for the example given above, verification of the document requires the stored code of the photograph and the code actually read to match one another. Other biometric parameters may also be used, such as fingerprints or parts thereof, which are then stored in coded form in the polymer chip. Here too, it is necessary for the coded, stored feature to be stable.

To illustrate the invention further, reference is made to the appended drawing, in which:

Fig. 1 shows a diagrammatic plan view of an embodiment of a banknote according to the invention;

Fig. 2 shows a cross section through the banknote illustrated in Fig. 1, along line I-I;

5 Fig. 3 shows a diagrammatic plan view of another embodiment of a banknote according to the invention;

Fig. 4 shows an enlarged illustration of an optically active element which is used in the banknote in accordance with Fig. 3;

10 Fig. 5 shows a cross section through the optically active element illustrated in Fig. 4;

Fig. 6 shows a cross section through yet another embodiment of a banknote according to the invention;

15 Fig. 7 shows a further embodiment of a security thread with polymeric chip;

Fig. 8 shows another embodiment of an optically active element with polymeric chip;

Fig. 9 shows a combination of a security thread and optically active element; and

20 Fig. 10 shows yet another embodiment of a security thread according to the invention, in cross section.

It should be noted that, in the figures which are to be discussed below, identical components are denoted by identical reference numerals.

25 Fig. 1 shows a banknote 1 made from paper. The banknote 1 comprises a security thread 2 containing a chip 3 made from a semiconductive organic polymer and conductive e.g. metallized parts 4. Furthermore, the banknote 1 comprises a second chip 3', which is likewise made from a semiconductive organic polymer. As can be seen from the cross section in accordance with Fig. 2, the security thread 2 is arranged on the paper 5, while the second polymeric chip 3' is embedded in the paper mass 5. The embedded chip 3' makes contact with a conductor or coil so as to provide the current required and readout.

30 Fig. 3 shows another embodiment of a banknote 1, in which the security thread 2, which again contains a polymeric chip and conductive parts 4, is incorporated in the paper mass. Sections of the conductive parts 4 are accessible via windows 6 in order to make direct electrical contact, if desired. The

banknote 1 illustrated in Fig. 3 also comprises a second chip 3', which in this case is situated beneath an optically active element 7. The optically active element 7 comprises conductive parts 8 which are separated by a strip 9 which is insulating, i.e. nonconductive. The chip 3' can be read and supplied with power via the conductive parts 8, whether directly or from a distance via capacitive coupling. The conductive part may be covered by a chemically inert layer, if readout is done capacitively. If direct contact is required, a part of the conductor and the entire part 9 can be covered in such a way that the IC and conductor are protected (with a nonconducting material), while other parts of the conductor are still accessible to direct contact.

Fig. 4 shows an enlarged view of the optical element 7 with chip 3', while Fig. 5 is a cross section through an optical element 7 of this nature.

Fig. 6 shows a further embodiment of a security thread 2 with a chip made from semiconductive organic polymer 3 and conductive parts 4, which is applied on the paper 5. In this embodiment, the polymer chip and sections of the conductive parts 4 of the security thread 2 are protected by means of a layer 10 of a chemically resistant, electrically nonconductive material. The protecting layer 10 may cover the entire thread, if capacitive coupling is used.

Fig. 7 shows yet another embodiment of a security thread according to the invention, in which the chip 3 does not form part of the security thread itself, but rather is positioned next to it. The conductive parts 4 of the security thread 2 are electrically insulated from one another by means of insulator block 4. The chip 3 is connected to the relevant conductive parts 4 of the security thread via electrical conductors 12.

An embodiment of the same type for an optically active element is illustrated in Fig. 8. The electrical conductors 12 provide the electrical contact between the conductive parts 8 of the optically active element and the polymeric chip 3'.

Fig. 9 shows a combination of a security thread 2 with optically active element 7, the metal parts 4 of the security thread 2 making electrical contact with the metal parts 8 of the optically active element 7. A chip made from semiconductive

organic material 3' is situated beneath the optically active element 7.

Fig. 10 illustrates yet another embodiment of a security thread according to the invention. In this embodiment, 5 the security thread is composed of a chip 3 and conductive parts 13, which are made from a conductive polymer. The security thread is arranged on the paper 5. The polymer chip 3 is protected with a layer 10 of chemically resistant material, which also covers (sections of) the conductive polymer 13. In order to 10 ensure a very good supply of power and readout, metal blocks 14 are arranged next to the layer 10 of insulating material, which metal blocks 14 are electrically connected to the conductive organic polymers 13.

In case of a system using capacitive coupling an additional 15 protecting layer may be applied over the metal parts 14 and the chemically resistant layer 10.

CLAIMS

1. Substrate which is made from paper and is provided with at least one integrated circuit, characterized in that the integrated circuit (3; 3') comprises a semiconductive organic polymer.

5 2. Substrate according to claim 1, characterized in that the organic polymer is selected from conjugated polymers.

3. Substrate according to claim 1 or 2, characterized in that the organic polymer is selected from oligomeric pentacene, poly(thienylene vinylene) or poly-3 alkylthiophene.

10 4. Substrate according to one of the preceding claims, characterized in that the integrated circuit is a contactlessly readable IC which can be read in an inductive or capacitive manner.

15 5. Substrate according to one of the preceding claims 1-3, characterized in that the substrate comprises a conductive security thread (2) which is connected to the integrated circuit (3) or circuits, which security thread (2) serves as a contact for readout operations and for current supply.

20 6. Substrate according to claim 5, characterized in that the integrated circuit (3) forms part of the security thread (2).

7. Substrate according to claim 4 or 5, characterized in that the security thread (2) has a thickness which lies in the range of from 5-60% of the thickness of the substrate.

25 8. Substrate according to one of the preceding claims 1-4, characterized in that the integrated circuit (3') forms part of an optical active element (7), such as a foil, hologram or kinegram.

30 9. Substrate according to one of the preceding claims, characterized in that the integrated circuit comprises a preprogrammed code which is applied before incorporating the circuit in the substrate.

10. Substrate according to one of the preceding claims, characterized in that the integrated circuit comprises a code of an intrinsic property of the substrate, which code, after the substrate has been produced, is arranged in the integrated circuit.

35 11. Substrate according to claim 9 or 10, characterized in

that the code is an encrypted code.

12. Substrate according to one of the preceding claims, characterized in that the substrate comprises additional security features.

5 13. Substrate according to claim 12, characterized in that an additional security feature is selected from a dye, fluorescent material, luminescent material or phosphorescent material.

10 14. Security paper comprising a substrate according to one of the preceding claims.

15. Security document comprising a substrate according to one of the preceding claims 1-13.

16. Security thread (2) comprising an insulating support (5) bearing an integrated circuit (3) made from a semiconductive organic polymer, provided with electrical contacts for the integrated circuit.

17. Optically active element (7) comprising an integrated circuit (3') made from a semiconductive organic polymer, provided with electrical contacts (8) for the integrated circuit.

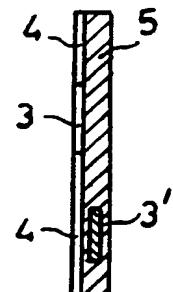
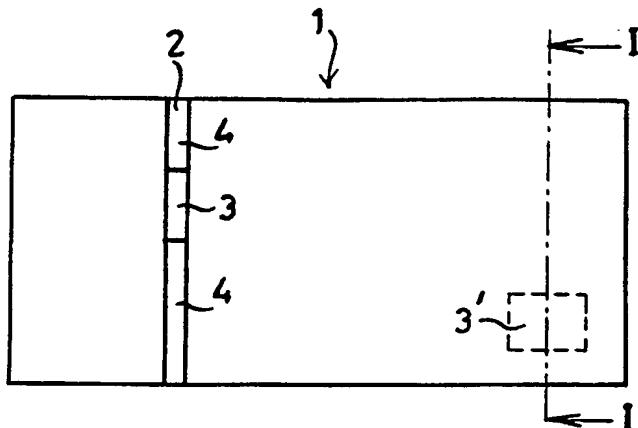


FIG. 2.

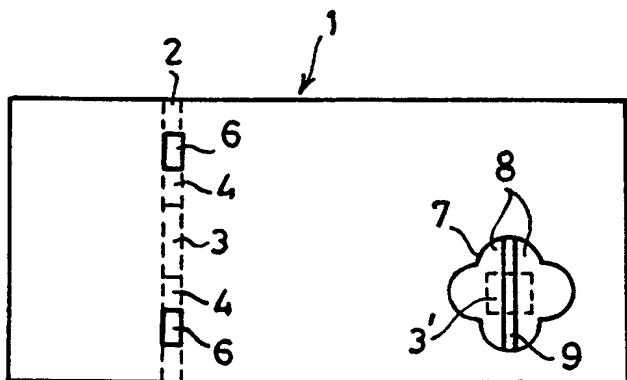


FIG. 3.

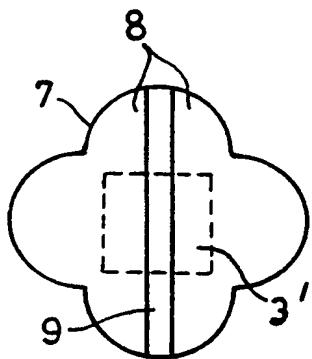


FIG. 4.

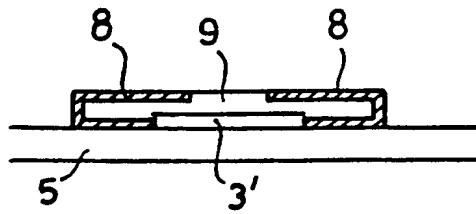


FIG. 5.

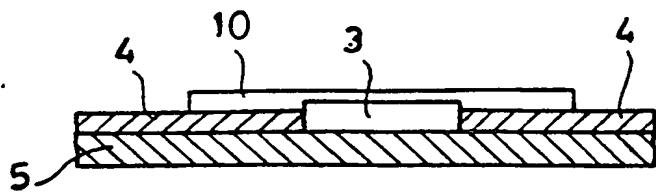


FIG.: 6.

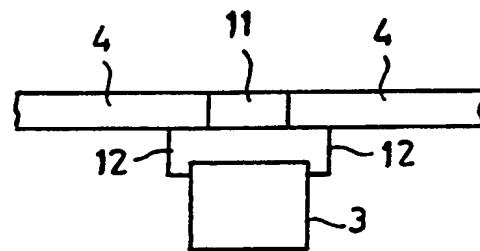
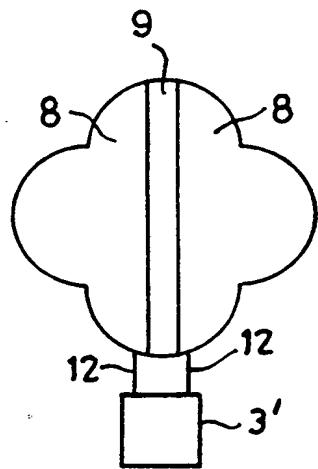


FIG.: 7.

FIG.: 8.

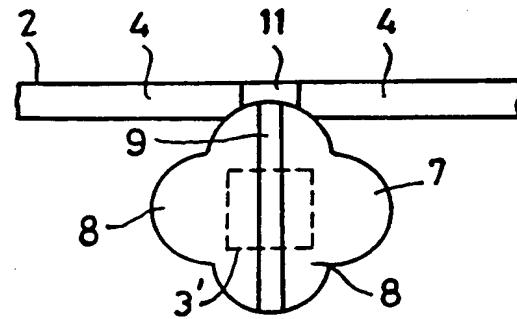


FIG.: 9.

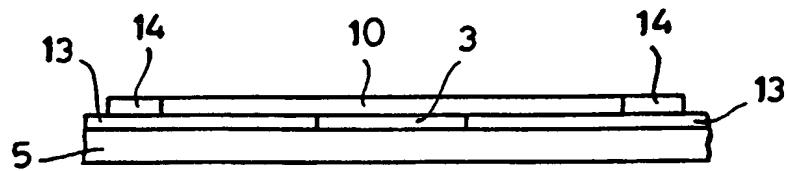


FIG.: 10.

# INTERNATIONAL SEARCH REPORT

Inte	Application No
PCT/NL 99/00220	

**A. CLASSIFICATION OF SUBJECT MATTER**  
 IPC 6 G06K19/02 G06K19/077

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)  
 IPC 6 G06K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	BROWN A R ET AL: "LOGIC GATES MADE FROM POLYMER TRANSISTORS AND THEIR USE IN RING OSCILLATORS" SCIENCE, vol. 270, 10 November 1995, pages 972-974, XP000644682 cited in the application see the whole document	1-4, 14, 15
Y A	----	8-13 5-7, 16, 17 -/-

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

\* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

30 June 1999

Date of mailing of the international search report

06/07/1999

Name and mailing address of the ISA  
 European Patent Office, P.B. 5818 Patentlaan 2  
 NL - 2280 HV Rijswijk  
 Tel. (+31-70) 340-2040, Tx. 31 651 epo nl.  
 Fax: (+31-70) 340-3016

Authorized officer

Goossens, A

# INTERNATIONAL SEARCH REPORT

International Application No

PCT/NL 99/00220

**A. CLASSIFICATION OF SUBJECT MATTER**  
 IPC 6 G06K19/02 G06K19/077

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 G06K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	BROWN A R ET AL: "LOGIC GATES MADE FROM POLYMER TRANSISTORS AND THEIR USE IN RING OSCILLATORS" SCIENCE, vol. 270, 10 November 1995, pages 972-974, XP000644682 cited in the application see the whole document	1-4, 14, 15
Y	---	8-13 5-7, 16, 17
A	---	-/-

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

\* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- "&" document member of the same patent family

Date of the actual completion of the international search

30 June 1999

Date of mailing of the international search report

06/07/1999

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Goossens, A

# INTERNATIONAL SEARCH REPORT

Information on patent family members

Inter	Application No
	PCT/NL 99/00220

Patent document cited in search report	Publication date	Patent family member(s)			Publication date
EP 0467219	A 22-01-1992	AT 170568 T	AU 635432 B	AU 8029991 A	15-09-1998 18-03-1993 23-01-1992
		CA 2043807 A	DE 69130093 D	DE 69130093 T	20-01-1992 08-10-1998 21-01-1999
		ES 2119749 T	JP 6022793 A	US 5202261 A	16-10-1998 01-02-1994 13-04-1993
		US 5250439 A			05-10-1993
WO 9724699	A 10-07-1997	AU 4311896 A			28-07-1997
US 4218674	A 19-08-1980	AU 1726776 A	BE 845935 A	BR 7605910 A	09-03-1978 31-12-1976 16-08-1977
		CH 607168 A	DE 2635795 A	FR 2324060 A	30-11-1978 17-03-1977 08-04-1977
		NL 7610007 A	JP 52033444 A	SE 7609944 A	14-03-1977 11-03-1977 10-03-1977
DE 19601358	A 25-07-1996	NONE			
EP 0753623	A 15-01-1997	IT MI951540 A			14-01-1997



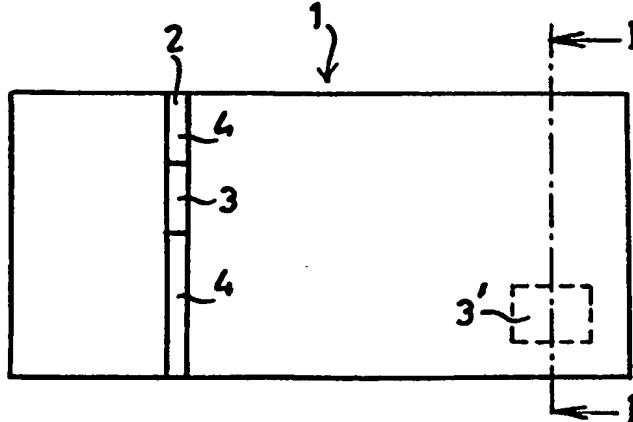
## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification <sup>6</sup> : <b>G06K 19/02, 19/077</b>		A1	(11) International Publication Number: <b>WO 99/54842</b>
			(43) International Publication Date: 28 October 1999 (28.10.99)
(21) International Application Number: PCT/NL99/00220		(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GD, GE, GH, GM, HR, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).	
(22) International Filing Date: 15 April 1999 (15.04.99)			
(30) Priority Data: 1008929 20 April 1998 (20.04.98) NL			
(71) Applicant (for all designated States except US): VHP VEILIGHEIDSPAPIERFABRIEK UGCHELEN B.V. [NL/NL]; Hoenderloseweg 84, NL-7339 GJ Apeldoorn (NL).			
(72) Inventors; and			
(75) Inventors/Applicants (for US only): KRUL, Johannes [NL/NL]; Meerkotlaan 117, NL-6641 ZB Beuningen (NL); de HESSE, Wilhelm, Bernardus [NL/NL]; Parkelerweg 20, NL-7391 ET Twello (NL).			
(74) Agent: VOLMER, J., C.; Exter Polak & Charlouis B.V., P.O. Box 3241, NL-2280 GE Rijswijk (NL).			

(54) Title: SUBSTRATE WHICH IS MADE FROM PAPER AND IS PROVIDED WITH AN INTEGRATED CIRCUIT

## (57) Abstract

The invention relates to a substrate (1) which is made from paper and is provided with at least one integrated circuit (3) which is produced from a semiconductive organic polymer. A semiconductive organic polymer of this nature, when used as the base material for the integrated circuit (3), leads to the possibility of directly producing the substrate (1) in the required thickness, to the need for support layers and/or protective layers being eliminated, and to the possibility of reducing the cost price of the substrate (1) compared to substrates which comprise an integrated circuit (3) of the silicon type.



**FOR THE PURPOSES OF INFORMATION ONLY**

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakhstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		

Substrate which is made from paper and is provided with an integrated circuit

The invention relates to a substrate which is made from paper and is provided with at least one integrated circuit.

A substrate of this nature is known from German Patent Application DE-196 01 358, and is used in security documents and banknotes to protect against forgery and fraud. This known substrate comprises an integrated circuit which is incorporated in the substrate and contains predetermined data. The IC is contactlessly readable and is joined to the substrate in a non-releasable manner. The IC which is used in this substrate is a conventional IC, i.e. of the well-known silicon type. The dimensions of the chip originally produced are reduced by means of etching or polishing, so that the chip acquires the thickness which is desired for it to be incorporated in the paper mass. In order to prevent damage to the crystalline circuits, the IC is reinforced with the aid of a support layer, which also serves to position the IC. Furthermore, the IC is covered with a protective, chemically resistant layer. The lack of flexibility of this known silicon chip is a drawback when a substrate of this nature is used as a security paper, for example in banknotes and identity documents. In addition, the extra layers which are to be included, as well as the additional processing step for producing the appropriate dimensions, lead to an additional increase in the cost price of a substrate of this nature.

The object of the present invention is to provide a paper-based substrate for use in security documents, banknotes and the like, in which an integrated circuit is incorporated, which substrate does not have the drawbacks mentioned above.

According to the present invention, this object is achieved with a substrate of the type mentioned above in which the integrated circuit comprises a semiconductive organic polymer. This means an electronic circuit which is arranged in the polymer material and the contents of which are programmed in order to assign it a specific function. Polymer chips of this nature are highly flexible and are therefore eminently suitable for use in security documents such as banknotes. Even sharp creases in the chip made from a semiconductive organic polymer

do not impede the functioning of the chip. In addition, polymeric ICs can be produced directly in the desired dimensions, in particular with regard to thickness, and the costs of a chip of this nature are lower by approximately a factor of 10 than the 5 current lowest price for a chip of the silicon type.

In the polymeric chip, the nonconductive support on which the semiconductive polymer material is deposited substantially determines the thickness of the overall integrated circuit. It is preferable to use a mechanically strong insulator: plastics with strong intramolecular and intermolecular interactions are particularly suitable for this purpose. 10

Using an IC of this nature as a security mark in security paper and similar items provides novel and powerful means of protection, since producing these ICs is much too 15 complicated for forgers and generally far exceeds their knowledge and capabilities.

In the context of this application, paper is understood to mean paper which is made from natural or synthetic fibres, as well as "paper" which can nowadays be produced from 20 plastic films, which paper is used for the production of security paper, banknotes and the like.

The integrated circuits may number one or more and may be adjusted as a function of the requirements. For example, with a view to certainty of operation, it is possible to incorporate 25 two or more identical polymer chips, so that in the event of one of these chips failing the substrate and/or the end product which has been produced therefrom can still be used.

Preferably, the organic polymer is selected from conjugated polymers, in particular from oligomeric pentacene, 30 poly(thienylene vinylene) or poly-3 alkylthiophene. An IC which is produced from one of these materials is described by Brown et al. in *Science*, 270, pp. 972-974, 1995.

As will be understood by the skilled person, the plastic IC, used in the invention, comprises additional layers 35 of different polymers besides the semiconductive polymer layer. For example, the substrate may be produced from polyimide, on which polyaniline blocks are formed, which function as source and drain. On top thereof, the semiconductive polymer layer is present, e.g. comprising poly(thienylene vinylene). This layer 40 is covered by an insulating layer, e.g. from polyvinylphenol,

while a top layer of polyaniline is the uppermost layer, which is the gate.

In an embodiment of the substrate according to the invention, the integrated circuit is contactlessly readable, the data transmission being brought about by an inductive or capacitive route, as is known in the prior art.

In case of inductive readout a coil is needed for the current supply, which has to be connected conductively to the IC; thereby making possible readout from a distance. In order to be able to readout at a small distance it is necessary that the IC contacts a conductor, wherein this conductor together with the measuring apparatus creates a capacity, whereby the current supply and readout become possible.

According to another preferred embodiment of the substrate according to the invention, the substrate comprises a conductive security thread which is connected to the integrated circuit, which security thread serves as a direct contact or indirect contact for the readout and for supplying the current. In a preferred embodiment thereof, the security thread is metallized in order to provide the required electrical conductivity, except at the position of the polymeric IC, where the deposition of metal is interrupted. In the case of direct current supply the metal has to be accessible. Possible ways of providing this accessibility comprise a security thread which is incorporated in the substrate, as well as a security thread which is incorporated in the substrate and the metal parts of which are accessible via so-called windows. Advantageously, one or more integrated circuits are part of the security thread itself. The thickness of this security thread may be adapted to the intended use of the substrate, for example in banknotes. For banknote paper, the thickness of the paper substrate usually lies in the range up to 100 µm. In this case, the thickness of the security thread preferably lies in the range of 15-60% of the thickness of this substrate. If the paper substrate has a different thickness, as for example for a cover of an identity document such as a passport, a minimum thickness of the security thread of approximately 10 µm applies. A thickness greater than 100 µm is relatively meaningless for use in security paper. The preferred embodiment of a polymeric IC in the form of a security thread provides an additional security feature which can easily

be recognized by the public. The thread comprising an integrated circuit may additionally comprise a number of other features, such as a dye, fluorescent or phosphorescent material, luminescent material and printed indices.

5         Organic, conductive polymers can also be used for supplying the current to the chip, although in case of direct contact the mechanical contact properties of these polymers currently still leave something to be desired.

10         A simple security thread which consists of conductive polymers is proposed in European Patent Application EP-A-0,753,623. However, a thread of this nature has only conductive properties. There are no semiconductor properties, and therefore it is not possible to apply and store a code in a manner which is comparable to a conductive polymer thread in which an integrated circuit is incorporated. The security thread comprising an integrated circuit according to the invention can be arranged in the customary way, for example by being incorporated completely or integrated in the paper mass, in a window or by being attached to the surface of a document. If protection 15 against attack from chemicals is required, a chemically resistant, electrically nonconductive protective layer may be applied 20 to the conductive organic polymers of the chip.

25         The polymeric chip does not per se have to be completely incorporated in the paper, as is the case for the silicon chip in the German patent application mentioned above. As an alternative, the polymeric IC may also be arranged on the surface of the substrate, using customary techniques for attaching foils, holograms, other optically active elements and the like.

30         The integrated circuit may also advantageously itself form part of all sorts of optically active elements, such as foils, patches, holograms or kinegrams, which are arranged on or in the substrate as additional security features. As has already been described above with regard to a security thread, according 35 to a further preferred embodiment it is also possible to shape optically active elements of this nature in such a manner that two electrically separate conductive parts of such elements function for readout and current supply, both directly and capacitively. The conductive parts may consist of metal, conductive polymer or a combination thereof.

For protection purposes, the integrated circuit may comprise a preprogrammed code, which code is applied before the chip is incorporated in the substrate.

Advantageously, the integrated circuit comprises a code of an intrinsic property of the substrate in which the circuit is incorporated.

In the current state of the art, the polymer IC may be used only in one direction; i.e. it can be written or programmed once. A preferred way of storing a code in the IC is to use techniques which are derived from cryptology. The authentic code is then stored in the IC in an encrypted manner, and deciphering is impossible without knowing the secret key. Thus, even if unwritten chips could be obtained illegally, the secret key forms a powerful and virtually impregnable barrier preventing the forger from applying a message to security documents and reading this message out. The protection can be improved still further if the optionally partial programming of the chip is carried out after the IC has become part of the security document, as will be explained in more detail below.

The shape of the polymer chip is not critical. Currently, a dimension of approximately 1 mm for a rectangular shape represents the lower limit of a surface dimension if a reasonable number of bits are to be stored in the IC. A rectangular IC of 4 mm by 6 mm currently holds approximately 48 bits, i.e. two bits/mm<sup>2</sup>. The ratio of the surface dimensions for a polymer chip (i.e. length to width) should preferably not exceed 10:1, owing to the resultant undesirable build-up of the chip given larger ratios. The small dimensions of an IC offer the possibility of covering the chip with additional features which are customarily used in the prior art. The dimensions of such additional features are generally large compared to the dimensions of the polymeric IC. Thus even large ICs, with sufficient memory capacity for storing large quantities of data, can be used without impairing the appearance of the security paper. If a combination of a chip with another security mark is arranged on the security paper, it is necessary to ensure that reading and supplying current to the chip are not adversely affected by an additional security feature of this nature.

The substrate comprising a polymer IC according to the invention is used as a security paper in, for example, bank-

notes, passports, identity cards and other security documents, such as securities.

The development of an inexpensive integrated circuit of this nature offers a number of novel possibilities for preventing forgery of security documents, beginning with a completely new type of electronics (electronic bar codes) in security paper.

Examples of the use of an IC as a security feature in a document which will be described are the various possibilities for a banknote, but comparable possibilities likewise exist for other types of security documents, such as passports, identity cards and the like.

A first possibility relates to the use of a completely preprogrammed IC in the substrate made from paper. The IC contains one or more codes, if desirable in encrypted form, relating to the banknote. This information may comprise the value, the country, the place and/or time of production, number and the like. For a specific value of the banknote, the information on each chip is substantially identical, i.e. value, country and usually the paper manufacturer and/or printer, and partially different, i.e. production time, production numbers and sometimes paper manufacturer and/or printer.

A more specific protection is obtained with a chip which is partially preprogrammed with a unique code (first code) and an additional second code. This second code is an encrypted translation of the first code. Encryption is carried out using a first key. In the event of verification, the second code is read and the encrypted relationship to the first code is verified using a second key. The second code can be applied to the chip before or after the chip has been arranged in the substrate. An encryption system of this nature is described, by way of example, in WO-A-97/24699.

In this known system, an intrinsic property of the object is coded, encrypted and enciphered. For banknotes, surface properties are taken at a special location, coded, encrypted and stored as a printed pattern on the banknote. In the event of verification, the printed pattern and the surface property are compared with one another using a second key.

Many other properties, as well as properties distributed arbitrarily in the substrate, are used in the prior art for

the protection of security documents, cf., inter alia, WO-A-91/19614 (fibre direction), GB-A-230,407 (reflective flakes), US-A-4,218,764 (magnetic particles or fibres) and WO-A-87/01845 (conductive fibres). In all these cases, arbitrary and therefore unique properties of a document are used for the verification. Hitherto, there has been no suitable chip available for use in paper substrates in order to store the (encrypted) code, and consequently a coded property was always stored in another way, for example outside the document itself, or was printed in or on the document or recorded magnetically therein. The polymeric chip which is used in the substrate according to the invention makes it technically possible to use and store these protection features inside the document.

The fluorescence properties of arbitrarily distributed fluorescent fibres in a predetermined area of the banknote may be a suitable property. However, any other property which can be measured and is arbitrarily distributed in or on the paper may also be used. A condition is that the property used must be stable throughout the entire lifespan of the document, which means that any property which is highly dependent on the consequences of use, such as soiling, contamination, creasing and the like, is in principle unsuitable.

The coordinates of the relevant part of the banknote where the arbitrary property is determined and, if necessary, the orientation in which the surface must be examined may also be stored in the chip. Thus, when verifying the banknote, a specific parameter is measured along a predetermined path, or an image of the entire banknote is taken, but the assessment is carried out using only the data found at the pre-coded coordinates. The result of this measurement is compared with the stored code, which likewise refers to the same property at the same location. On the basis of this comparison, which may optionally be encrypted, a rejection or acceptance signal is generated.

The substrate with a polymeric IC according to the invention may furthermore comprise customary security features, such as watermarks, security threads, optically active elements and special chemicals, microprints, etc., standard techniques being used to determine these features.

The invention also relates to a security thread or

optically active element comprising an integrated circuit made from a semiconductive organic polymer.

The following example illustrates the invention. In this case, specific fluorescent properties in a specific part of the document are used as an example. Many banknotes are supplied with a number of highly fluorescent fibres which emit different colours of light. These fibres are distributed arbitrarily through the document. The local fluorescence of various types of fibres at a predetermined location can be coded and digitally stored in the chip, optionally in encrypted form, at the time the document is produced, i.e. during the paper manufacturing phase or during the document printing phase. In the event of verification, the area in question is read again using the coordinates and orientation which are stored in the chip, and the results are compared with one another, followed by rejection or acceptance. The said coordinates and orientation will generally differ for each separate banknote, with the result that verification is completely unique for the said document, since the arbitrary property and coordinates are unique for that document. In this way, the chip of each separate banknote contains a unique code which, as it were, represents a fingerprint of a specific part of the banknote in question. The code of the intrinsic property may be stored either in encrypted or nonencrypted form.

As has already been mentioned above, the use of the substrate according to the invention is not limited to banknotes. For other uses, such as passports and identity documents, part of a biometric property of the legal owner can be used to generate a digital code which is then stored in the IC of the document. One such example could be a coded part of a digitized photograph of the legal owner, the part to be digitized being determined by coded parameters which are unique for each document. Entirely as for the example given above, verification of the document requires the stored code of the photograph and the code actually read to match one another. Other biometric parameters may also be used, such as fingerprints or parts thereof, which are then stored in coded form in the polymer chip. Here too, it is necessary for the coded, stored feature to be stable.

To illustrate the invention further, reference is made to the appended drawing, in which:

Fig. 1 shows a diagrammatic plan view of an embodiment of a banknote according to the invention;

Fig. 2 shows a cross section through the banknote illustrated in Fig. 1, along line I-I;

Fig. 3 shows a diagrammatic plan view of another embodiment of a banknote according to the invention;

Fig. 4 shows an enlarged illustration of an optically active element which is used in the banknote in accordance with Fig. 3;

Fig. 5 shows a cross section through the optically active element illustrated in Fig. 4;

Fig. 6 shows a cross section through yet another embodiment of a banknote according to the invention;

Fig. 7 shows a further embodiment of a security thread with polymeric chip;

Fig. 8 shows another embodiment of an optically active element with polymeric chip;

Fig. 9 shows a combination of a security thread and optically active element; and

Fig. 10 shows yet another embodiment of a security thread according to the invention, in cross section.

It should be noted that, in the figures which are to be discussed below, identical components are denoted by identical reference numerals.

Fig. 1 shows a banknote 1 made from paper. The banknote 1 comprises a security thread 2 containing a chip 3 made from a semiconductive organic polymer and conductive e.g. metallized parts 4. Furthermore, the banknote 1 comprises a second chip 3', which is likewise made from a semiconductive organic polymer. As can be seen from the cross section in accordance with Fig. 2, the security thread 2 is arranged on the paper 5, while the second polymeric chip 3' is embedded in the paper mass 5. The embedded chip 3' makes contact with a conductor or coil so as to provide the current required and readout.

Fig. 3 shows another embodiment of a banknote 1, in which the security thread 2, which again contains a polymeric chip and conductive parts 4, is incorporated in the paper mass. Sections of the conductive parts 4 are accessible via windows 6 in order to make direct electrical contact, if desired. The

banknote 1 illustrated in Fig. 3 also comprises a second chip 3', which in this case is situated beneath an optically active element 7. The optically active element 7 comprises conductive parts 8 which are separated by a strip 9 which is insulating, i.e. nonconductive. The chip 3' can be read and supplied with power via the conductive parts 8, whether directly or from a distance via capacitive coupling. The conductive part may be covered by a chemically inert layer, if readout is done capacitively. If direct contact is required, a part of the conductor and the entire part 9 can be covered in such a way that the IC and conductor are protected (with a nonconducting material), while other parts of the conductor are still accessible to direct contact.

Fig. 4 shows an enlarged view of the optical element 7 with chip 3', while Fig. 5 is a cross section through an optical element 7 of this nature.

Fig. 6 shows a further embodiment of a security thread 2 with a chip made from semiconductive organic polymer 3 and conductive parts 4, which is applied on the paper 5. In this embodiment, the polymer chip and sections of the conductive parts 4 of the security thread 2 are protected by means of a layer 10 of a chemically resistant, electrically nonconductive material. The protecting layer 10 may cover the entire thread, if capacitive coupling is used.

Fig. 7 shows yet another embodiment of a security thread according to the invention, in which the chip 3 does not form part of the security thread itself, but rather is positioned next to it. The conductive parts 4 of the security thread 2 are electrically insulated from one another by means of insulator block 4. The chip 3 is connected to the relevant conductive parts 4 of the security thread via electrical conductors 12.

An embodiment of the same type for an optically active element is illustrated in Fig. 8. The electrical conductors 12 provide the electrical contact between the conductive parts 8 of the optically active element and the polymeric chip 3'.

Fig. 9 shows a combination of a security thread 2 with optically active element 7, the metal parts 4 of the security thread 2 making electrical contact with the metal parts 8 of the optically active element 7. A chip made from semiconductive

organic material 3' is situated beneath the optically active element 7.

Fig. 10 illustrates yet another embodiment of a security thread according to the invention. In this embodiment, 5 the security thread is composed of a chip 3 and conductive parts 13, which are made from a conductive polymer. The security thread is arranged on the paper 5. The polymer chip 3 is protected with a layer 10 of chemically resistant material, which also covers (sections of) the conductive polymer 13. In order to 10 ensure a very good supply of power and readout, metal blocks 14 are arranged next to the layer 10 of insulating material, which metal blocks 14 are electrically connected to the conductive organic polymers 13.

In case of a system using capacitive coupling an additional 15 protecting layer may be applied over the metal parts 14 and the chemically resistant layer 10.

CLAIMS

1. Substrate which is made from paper and is provided with at least one integrated circuit, characterized in that the integrated circuit (3; 3') comprises a semiconductive organic polymer.

5 2. Substrate according to claim 1, characterized in that the organic polymer is selected from conjugated polymers.

3. Substrate according to claim 1 or 2, characterized in that the organic polymer is selected from oligomeric pentacene, poly(thienylene vinylene) or poly-3 alkylthiophene.

10 4. Substrate according to one of the preceding claims, characterized in that the integrated circuit is a contactlessly readable IC which can be read in an inductive or capacitive manner.

15 5. Substrate according to one of the preceding claims 1-3, characterized in that the substrate comprises a conductive security thread (2) which is connected to the integrated circuit (3) or circuits, which security thread (2) serves as a contact for readout operations and for current supply.

20 6. Substrate according to claim 5, characterized in that the integrated circuit (3) forms part of the security thread (2).

7. Substrate according to claim 4 or 5, characterized in that the security thread (2) has a thickness which lies in the range of from 5-60% of the thickness of the substrate.

25 8. Substrate according to one of the preceding claims 1-4, characterized in that the integrated circuit (3') forms part of an optical active element (7), such as a foil, hologram or kinegram.

30 9. Substrate according to one of the preceding claims, characterized in that the integrated circuit comprises a preprogrammed code which is applied before incorporating the circuit in the substrate.

10. Substrate according to one of the preceding claims, characterized in that the integrated circuit comprises a code of an intrinsic property of the substrate, which code, after the substrate has been produced, is arranged in the integrated circuit.

35 11. Substrate according to claim 9 or 10, characterized in

that the code is an encrypted code.

12. Substrate according to one of the preceding claims, characterized in that the substrate comprises additional security features.

5 13. Substrate according to claim 12, characterized in that an additional security feature is selected from a dye, fluorescent material, luminescent material or phosphorescent material.

10 14. Security paper comprising a substrate according to one of the preceding claims.

15. Security document comprising a substrate according to one of the preceding claims 1-13.

16. Security thread (2) comprising an insulating support (5) bearing an integrated circuit (3) made from a semiconductive organic polymer, provided with electrical contacts for the integrated circuit.

17. Optically active element (7) comprising an integrated circuit (3') made from a semiconductive organic polymer, provided with electrical contacts (8) for the integrated circuit.

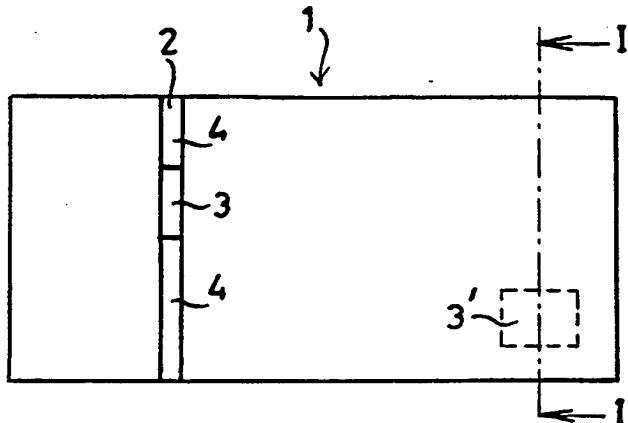


FIG: 1.

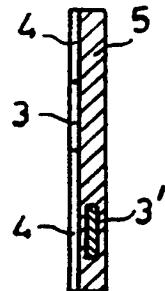


FIG: 2.

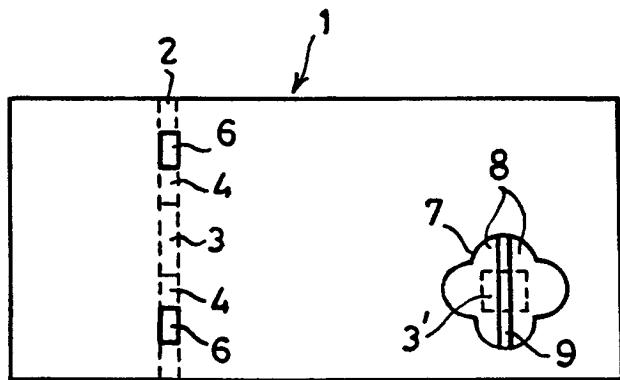


FIG: 3.

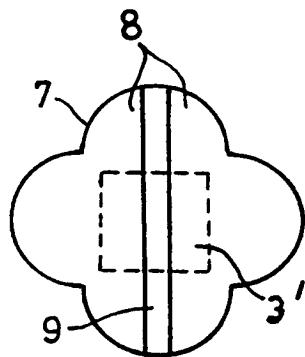


FIG: 4.

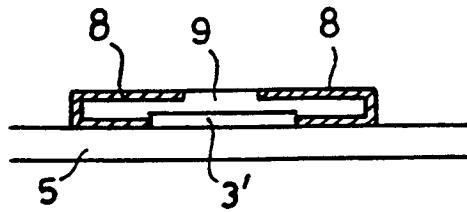


FIG: 5.

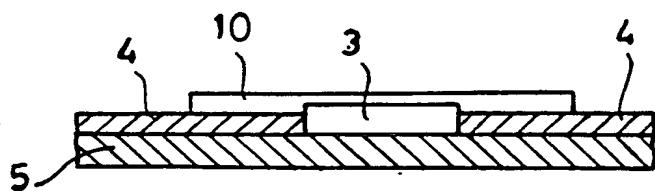


FIG: 6.

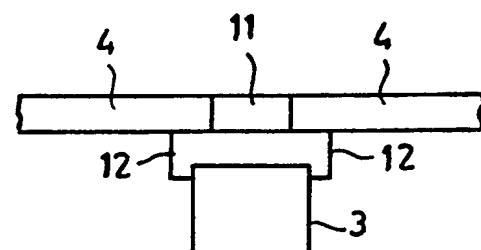
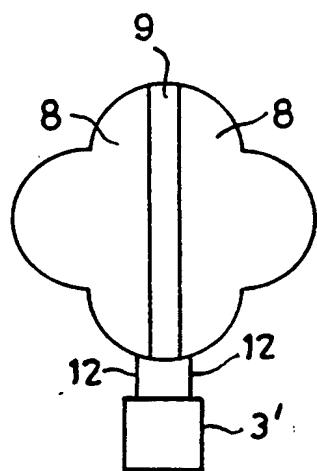


FIG: 7.

FIG: 8.

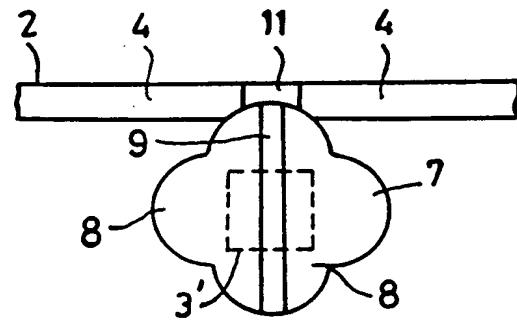


FIG: 9.

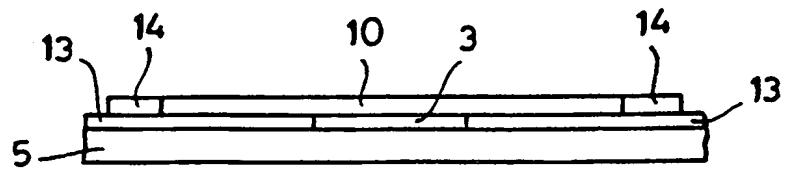


FIG: 10.

# INTERNATIONAL SEARCH REPORT

Inte... Application No  
PCT/NL 99/00220

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 G06K19/02 G06K19/077

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 G06K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	BROWN A R ET AL: "LOGIC GATES MADE FROM POLYMER TRANSISTORS AND THEIR USE IN RING OSCILLATORS" SCIENCE, vol. 270, 10 November 1995, pages 972-974, XP000644682 cited in the application see the whole document	1-4, 14, 15
Y		8-13
A		5-7, 16, 17
	---	-/-

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

### \* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

30 June 1999

Date of mailing of the international search report

06/07/1999

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Goossens, A

## INTERNATIONAL SEARCH REPORT

International Application No

PCT/NL 99/00220

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	BONNOIT A ET AL: "EVALUATION DES PERFORMANCES DE CIRCUITS LOGIQUES REALISES LOGIQUES REALISES A PARTIR DE TRANSISTORS MIS ORGANIQUES PERFORMANCE EVALUATION OF LOGIC CIRCUITS USING MIS TRANSISTORS" ONDE ELECTRIQUE, vol. 74, no. 4, 1 July 1994, pages 18-24, XP000438846 see page 18 ----	1
A	EP 0 467 219 A (MILES INC) 22 January 1992 see page 3, line 9 - line 19 see page 6, line 23 - line 41 ----	3
Y	WO 97 24699 A (S E AXIS LIMITED ;KARIAKIN YOURY D (BY)) 10 July 1997 cited in the application see the whole document ----	8-11
Y	US 4 218 674 A (BROSOW JORGEN ET AL) 19 August 1980 cited in the application see column 4, line 48 - column 5, line 20 ----	12,13
A	DE 196 01 358 A (FRAUNHOFER GES FORSCHUNG) 25 July 1996 cited in the application see the whole document -----	1,4, 12-15
A	EP 0 753 623 A (MANTEGAZZA A ARTI GRAFICI) 15 January 1997 cited in the application see the whole document -----	1,5-7,16

**INTERNATIONAL SEARCH REPORT**

Information on patent family members

Inte[redacted] Application No

PCT/NL 99/00220

Patent document cited in search report	Publication date	Patent family member(s)		Publication date
EP 0467219 A	22-01-1992	AT AU AU CA DE DE ES JP US US	170568 T 635432 B 8029991 A 2043807 A 69130093 D 69130093 T 2119749 T 6022793 A 5202261 A 5250439 A	15-09-1998 18-03-1993 23-01-1992 20-01-1992 08-10-1998 21-01-1999 16-10-1998 01-02-1994 13-04-1993 05-10-1993
WO 9724699 A	10-07-1997	AU	4311896 A	28-07-1997
US 4218674 A	19-08-1980	AU BE BR CH DE FR JP NL SE	1726776 A 845935 A 7605910 A 607168 A 2635795 A 2324060 A 52033444 A 7610007 A 7609944 A	09-03-1978 31-12-1976 16-08-1977 30-11-1978 17-03-1977 08-04-1977 14-03-1977 11-03-1977 10-03-1977
DE 19601358 A	25-07-1996	NONE		
EP 0753623 A	15-01-1997	IT	MI951540 A	14-01-1997

**This Page is Inserted by IFW Indexing and Scanning  
Operations and is not part of the Official Record**

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- BLACK BORDERS**
- IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- FADED TEXT OR DRAWING**
- BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- SKEWED/SLANTED IMAGES**
- COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- GRAY SCALE DOCUMENTS**
- LINES OR MARKS ON ORIGINAL DOCUMENT**
- REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- OTHER:** \_\_\_\_\_

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.**

THIS PAGE BLANK (USPTO)